

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	7	"5,668,568"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 09:06
L19	23	("5678064").URPN.	USPAT	OR	ON	2004/11/17 09:37
L20	109	((first and second) adj3 (portion) adj2 (ram (random adj access adj memory)))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 09:41
L21	8	20 and (cpu and dma)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 09:38
L22	182	((ram (random adj access adj memory)) with partition\$3) and (cpu and dma)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 09:41
L23	23	((ram (random adj access adj memory)) with partition\$3 with portion) and (cpu and dma)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 09:45
L24	26	((dram ram (random adj access adj memory)) with partition\$3 with portion) and (cpu and dma)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 09:51
L25	19	(single adj5 (ram dram sdram)) same (cpu and dma)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 10:24
L26	34	(cpu with dma with shar\$3 with (ram dram sdram))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 11:29
L27	8	(cpu with dma with (portion adj3 (ram dram sdram)))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 11:35
L28	2	(cpu with dma with partition\$3 with (ram dram sdram))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 11:36
L29	36	(cpu with dma with (partition\$3 shar\$3) with (ram dram sdram))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 11:39
L30	178	((ram dram sdram) with (separat\$3 adj2 portion))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 11:39
L31	0	((ram dram sdram) with (separat\$3 adj2 portion)) same (cpu and dma)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 11:40
L32	32	((ram dram sdram) with (separat\$3 adj2 portion)) and (cpu and dma)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 11:40
L33	1	10/621997	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 11:42

L34	5	("4456965" "4912636" "5345566" "5495585" "6115767").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2004/11/17 11:46
L35	36	("4912636").URPN.	USPAT	OR	ON	2004/11/17 11:57
L36	8	((cpu and dma) with (portion adj3 (ram dram sdram)))	USPAT	OR	ON	2004/11/17 12:01
L37	61	(ram dram sdram) with partition with (first and second)	USPAT	OR	ON	2004/11/17 13:11
L38	1	(ram dram sdram) with partition with ((first and second) adj2 (portion block region area))	USPAT	OR	ON	2004/11/17 12:02
L40	1	("5,774,684").PN.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	OFF	2004/11/17 13:11
L41	28	("5774684").URPN.	USPAT	OR	ON	2004/11/17 13:12
L42	258	345/537.ccls.	US-PGPUB; USPAT	OR	ON	2004/11/17 14:14
L43	55	42 and (cpu and dma)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/17 14:14
S1	956	710/22	US-PGPUB; USPAT	OR	ON	2004/11/15 10:37
S2	39	710/22 and lcd	US-PGPUB; USPAT	OR	ON	2003/01/22 13:54
S7	1014	710/22-28.ccls.	US-PGPUB; USPAT	OR	ON	2003/01/22 15:20
S10	2013	dma and ((first and second) near (buffer or memory))	US-PGPUB; USPAT	OR	ON	2003/01/22 17:15
S18	4441	dma and ((first and second) near3 (memory or buffer))	US-PGPUB; USPAT	OR	ON	2003/01/24 12:43
S19	672	(dma and ((first and second) near3 (memory or buffer))) and (delay with (cpu or microprocessor or processor))	US-PGPUB; USPAT	OR	ON	2003/01/24 13:06
S20	15	((dma and ((first and second) near3 (memory or buffer))) and (delay with (cpu or microprocessor or processor))) and gps	US-PGPUB; USPAT	OR	ON	2003/01/24 13:03
S21	271	((dma and ((first and second) near3 (memory or buffer))) and (delay with (cpu or microprocessor or processor))) and (select\$3) and (isolat\$3)	US-PGPUB; USPAT	OR	ON	2003/01/24 13:08
S22	1015	710/22-28.ccls.	US-PGPUB; USPAT	OR	ON	2003/01/24 13:09
S23	796	710/22-28.ccls. and dma	US-PGPUB; USPAT	OR	ON	2003/01/24 13:09
S24	726	(710/22-28.ccls. and dma) and (first and second) and (memory or buffer)	US-PGPUB; USPAT	OR	ON	2003/01/24 13:42
S25	352	((710/22-28.ccls. and dma) and (first and second) and (memory or buffer)) and (delay\$3 or suspend\$4)	US-PGPUB; USPAT	OR	ON	2003/01/24 13:10
S27	356	(710/22-28.ccls. and dma) and ((first and second) near3 (memory or buffer))	US-PGPUB; USPAT	OR	ON	2003/01/24 13:49
S28	243	((710/22-28.ccls. and dma) and ((first and second) near3 (memory or buffer))) and cpu	US-PGPUB; USPAT	OR	ON	2003/01/24 13:49
S29	49	((710/22-28.ccls. and dma) and ((first and second) near3 (memory or buffer))) and cpu and ((hold or suspend or delay) with cpu)	US-PGPUB; USPAT	OR	ON	2003/01/24 14:15
S31	10	casey near david	US-PGPUB; USPAT	OR	ON	2003/01/24 14:18
S32	76	345/538	US-PGPUB; USPAT	OR	ON	2004/11/17 14:13
S33	17	345/538 and dma	US-PGPUB; USPAT	OR	ON	2003/01/24 14:48

S34	2	("6226099" "6369910").PN.	USPAT	OR	ON	2003/01/24 14:32
S35	10	"4980828".URPN.	USPAT	OR	ON	2003/01/24 14:38
S36	95	709/212.ccls. and dma	US-PGPUB; USPAT	OR	ON	2003/01/24 14:48
S37	55	(709/212.ccls. and dma) and cpu	US-PGPUB; USPAT	OR	ON	2003/01/24 14:48
S38	10	("4583160" "4959782" "5179663" "5392404" "5499345" "5544346" "5974479" "5996037" "6105082" "6115767"). PN.	USPAT	OR	ON	2003/01/24 15:00
S39	0	"6463482".URPN.	USPAT	OR	ON	2003/01/24 15:04
S40	200	710/25	US-PGPUB; USPAT	OR	ON	2003/01/24 15:05
S41	102	710/25 and dma	US-PGPUB; USPAT	OR	ON	2003/01/24 15:05
S42	99	(710/25 and dma) and (cpu or microprocessor or controller)	US-PGPUB; USPAT	OR	ON	2003/01/24 15:05
S43	13	((710/25 and dma) and (cpu or microprocessor or controller)) and ((delay or hold or suspend or stop) with cpu)	US-PGPUB; USPAT	OR	ON	2003/01/24 15:28
S44	45	((710/25 and dma) and (cpu or microprocessor or controller)) and ((delay or hold or suspend or stop) with transfer\$4)	US-PGPUB; USPAT	OR	ON	2003/01/24 15:07
S45	5	("4706221" "5175864" "5404335" "5530901" "5561816"). PN.	USPAT	OR	ON	2003/01/24 15:21
S46	1	"5732284".URPN.	USPAT	OR	ON	2003/01/24 15:26
S47	88	DMA AND GPS AND ANTENNA	US-PGPUB; USPAT	OR	ON	2003/01/24 15:31
S48	8	(DMA near transfer\$4) same (bypass\$4 with cpu)	US-PGPUB; USPAT	OR	ON	2003/01/24 15:32
S49	7	"5542110".URPN.	USPAT	OR	ON	2003/01/24 15:39
S50	1	"6,348,913"	US-PGPUB; USPAT	OR	ON	2003/01/24 17:05
S51	109	"5,530,455"	US-PGPUB; USPAT	OR	ON	2003/01/24 17:06
S52	353	dma and gps	US-PGPUB; USPAT	OR	ON	2003/01/24 17:38
S53	88	(dma and gps) and antenna	US-PGPUB; USPAT	OR	ON	2003/01/24 17:38
S54	109	dma and (gps near receiver)	US-PGPUB; USPAT	OR	ON	2003/01/24 17:38
S55	450	ram same (block) same partition\$3	US-PGPUB; USPAT	OR	ON	2003/01/27 09:31
S56	369	(ram same (block) same partition\$3) and (first and second) and portion	US-PGPUB; USPAT	OR	ON	2003/01/27 09:31
S57	9	ram same (block) same partition\$3 same ((first and second) and portion)	US-PGPUB; USPAT	OR	ON	2003/01/27 09:37
S58	643	711/104	US-PGPUB; USPAT	OR	ON	2003/01/27 09:38
S59	34	711/104 and ram and block and partition	US-PGPUB; USPAT	OR	ON	2003/01/27 09:38
S62	0	suppress near cpu near clock	US-PGPUB; USPAT	OR	ON	2003/06/17 13:36
S63	1	suppressing near cpu near clock	US-PGPUB; USPAT	OR	ON	2003/06/17 13:45
S64	59	((suppress\$3 or stall\$3 or hold\$3) near3 cpu near2 clock)	US-PGPUB; USPAT	OR	ON	2003/06/17 13:51
S65	35	((((suppress\$3 or stall\$3 or hold\$3) near3 cpu near2 clock)) and dma	US-PGPUB; USPAT	OR	ON	2003/06/17 13:46

S66	107	((suppress\$3 or stall\$3 or hold\$3 or halt\$3) near3 cpu near2 clock)	US-PGPUB; USPAT	OR	ON	2003/06/17 13:51
S67	43	((((suppress\$3 or stall\$3 or hold\$3 or halt\$3) near3 cpu near2 clock)) and dma	US-PGPUB; USPAT	OR	ON	2003/06/24 17:20
S68	1684	710/100	US-PGPUB; USPAT	OR	ON	2003/06/24 17:21
S69	55	710/100 and ((suspend\$3 or halt\$4 or suppress\$4 or stop\$4) near2 clock)	US-PGPUB; USPAT	OR	ON	2003/06/24 17:24
S70	22	(710/100 and ((suspend\$3 or halt\$4 or suppress\$4 or stop\$4) near2 clock)) and dma	US-PGPUB; USPAT	OR	ON	2003/06/24 17:30
S71	1566	711/112	US-PGPUB; USPAT	OR	ON	2003/06/24 17:30
S72	878	710/22.ccls.	US-PGPUB; USPAT	OR	ON	2004/11/15 10:40
S73	224	710/22.ccls. and ((first and second) near2 memory)	US-PGPUB; USPAT	OR	ON	2004/11/15 10:42
S74	1	10/621997	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 10:43
S75	614	dma with refresh\$3	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 10:43
S77	1	10/621997	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 13:20
S78	142	710/23.ccls. and (dma (direct adj memory adj access))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 13:21
S79	133	S78 and (memory with access\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 13:21
S80	121	S78 and (memory with access\$3 with (cpu dma))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 13:28
S81	23	S78 and (cpu with (delay\$3 suspen\$4 prevent\$3 wait\$3))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 13:28
S83	59	((dma and cpu) with portion with memory) and (cpu with (delay\$3 suspen\$4 prevent\$3 wait\$3))	USPAT	OR	ON	2004/11/15 15:46
S85	1161	(dma adj2 control\$4) and (cpu with (delay\$3 suspen\$4 prevent\$3 wait\$3))	USPAT	OR	ON	2004/11/15 15:47
S86	237	S85 and ((first and second) adj2 memory)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 15:47
S87	366	(dma adj2 control\$4) and (cpu with (delay\$3 suspen\$4 prevent\$3 wait\$3) with memory)	USPAT	OR	ON	2004/11/15 15:47
S88	115	S87 and ((first and second) adj2 memory)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 16:21

S89	26	(suppressing with clock with cpu)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 16:22
S90	2	(suppressing with clock with cpu with delay\$3)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/15 16:22
S91	6	("5276852" "5499353" "5802569" "5822616" "6006317" "6012106").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2004/11/15 16:26
S92	1906	((stop\$4 suppress\$3 hold\$3) with clock with cpu)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/16 10:54
S93	388	((stop\$4 suppress\$3 hold\$3) adj5 clock adj5 cpu)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/16 11:03
S94	94	S93 and (dma near3 control\$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/16 11:04
S95	17	S93 same (dma near3 control\$4)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/16 11:47
S97	12	SUMIOKA near KAZUO	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/16 11:47
S98	3	("5968145").URPN.	USPAT	OR	ON	2004/11/16 13:44
S99	120	(cpu and dma and (display adj control\$4)) same (chip (integrated adj circuit) ic)	USPAT	OR	ON	2004/11/16 13:46
S10 0	64	(cpu and dma and (display adj control\$4)) with (chip (integrated adj circuit) ic)	USPAT	OR	ON	2004/11/16 13:49
S10 1	28	("5774684").URPN.	USPAT	OR	ON	2004/11/16 14:22
S10 2	47	(gps adj2 receiver) and (cpu and dma and (chip (integrated adj circuit) ic))	USPAT	OR	ON	2004/11/16 14:23
S10 3	8	("4651157" "5331307" "5334974" "5457438" "5463595" "5479157" "5510765" "5682133").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2004/11/16 14:36
S10 4	32	("5986543").URPN.	USPAT	OR	ON	2004/11/16 14:38
S10 5	3	("5968145").URPN.	USPAT	OR	ON	2004/11/16 14:41
S10 6	131	(gps (global adj position\$3 adj system)) and (cpu and dma and (display near2 control\$4))	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/16 14:45
S10 7	426	"5,223,844"	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/16 14:49
S10 8	4	S107 and (gps and cpu and dma)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2004/11/16 14:49